

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT	Richard Michael Taylor	CONFIRMATION NO. 2918
APPLICATION NO.	10/518,974	
FILING DATE:	December 21, 2004	
TITLE	MICROPROCESSOR INSTRUCTION EXECUTION METHOD FOR EXPLOITING PARALLELISM BY TIME ORDERING OPERATIONS IN A SINGLE THREAD AT COMPILE TIME	

FILED ELECTRONICALLY ON January 6, 2010

MAIL STOP ISSUE FEE
COMMISSIONER OF PATENTS
P.O. BOX 1450
ALEXANDRIA, VA 22313-1450

Attn.: Official Draftsman

**SUBMISSION OF REPLACEMENT DRAWINGS IN RESPONSE
TO NOTICE OF ALLOWANCE/ALLOWABILITY, MAILED DECEMBER 1, 2009**

Dear Sir:

In response to the Notice of Allowance and Notice of Allowability, mailed by the Patent Office on December 1, 2009, applicant submits herewith twenty-five (25) sheets of replacement drawings for the above-referenced application.

Specifically, in accordance with the objections raised by the draftsman in the Notice of Draftsperson's Patent Drawing Review, dated October 28, 2009, mailed with the Notice of Allowance on December 1, 2009, applicant has amended all of the figures by enlarging them and making them clearer. In order to fit the enlarged drawings on the page within the required

margins, applicant placed only one figure on each sheet of drawing. Accordingly, applicant submits herewith 25 sheets of drawings, which replace the original 13 sheets of drawings filed with the application.

The Commissioner is authorized to charge any fees associated with this communication to Deposit Account 50-4364.

Respectfully submitted,

Date: January 6, 2010

By /Mark D. Simpson/
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